

FIG. 1

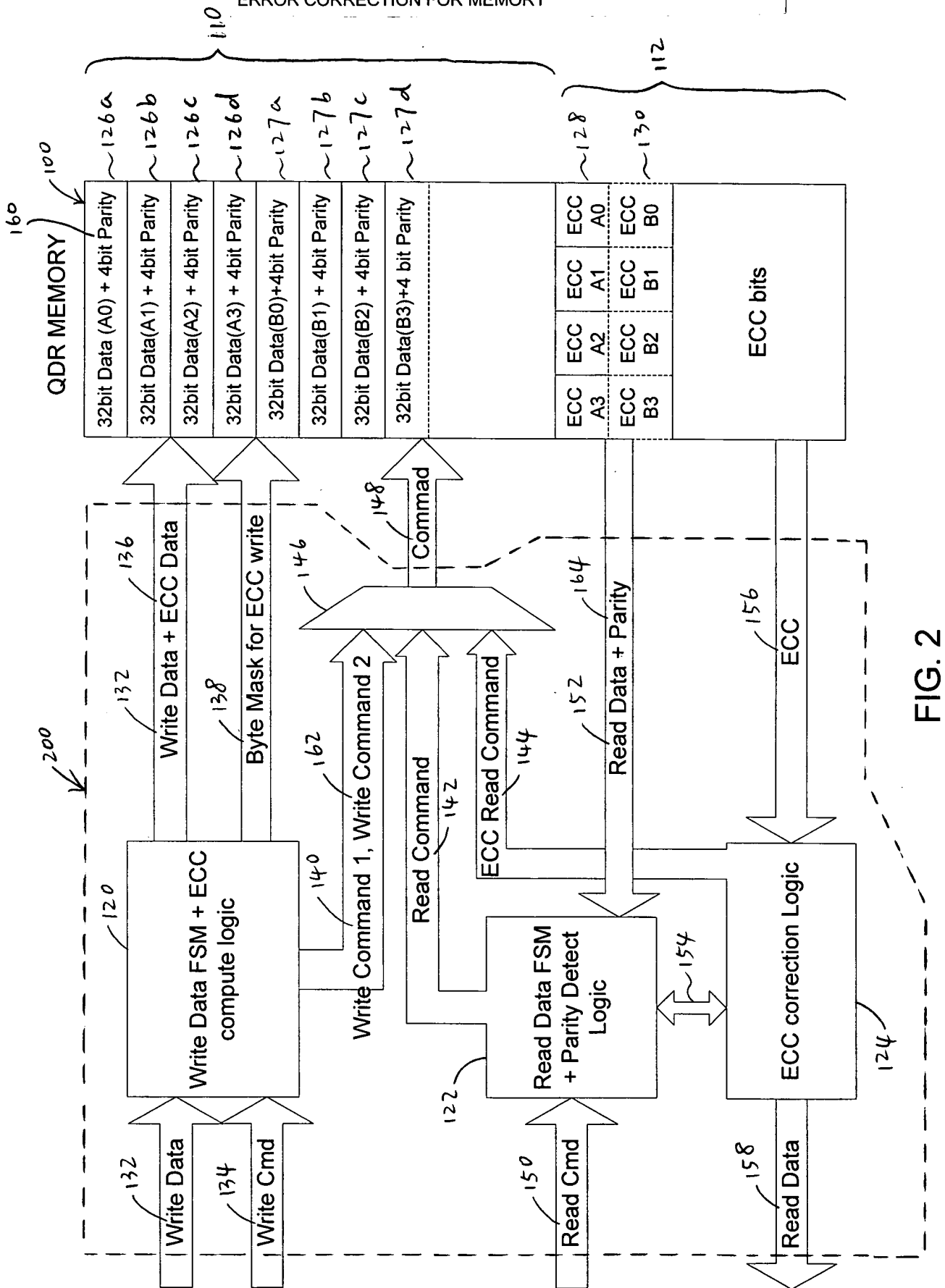
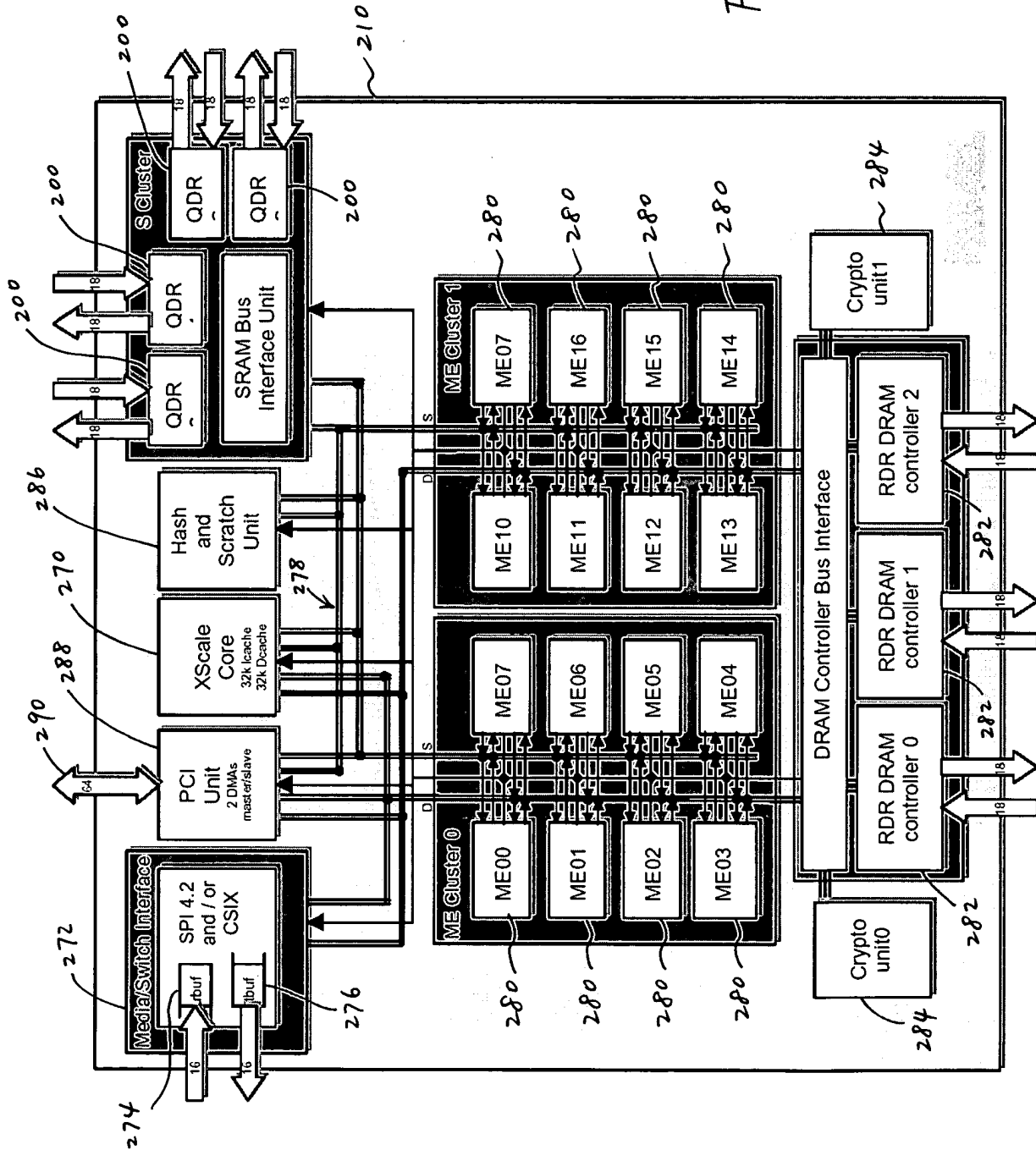


FIG. 3



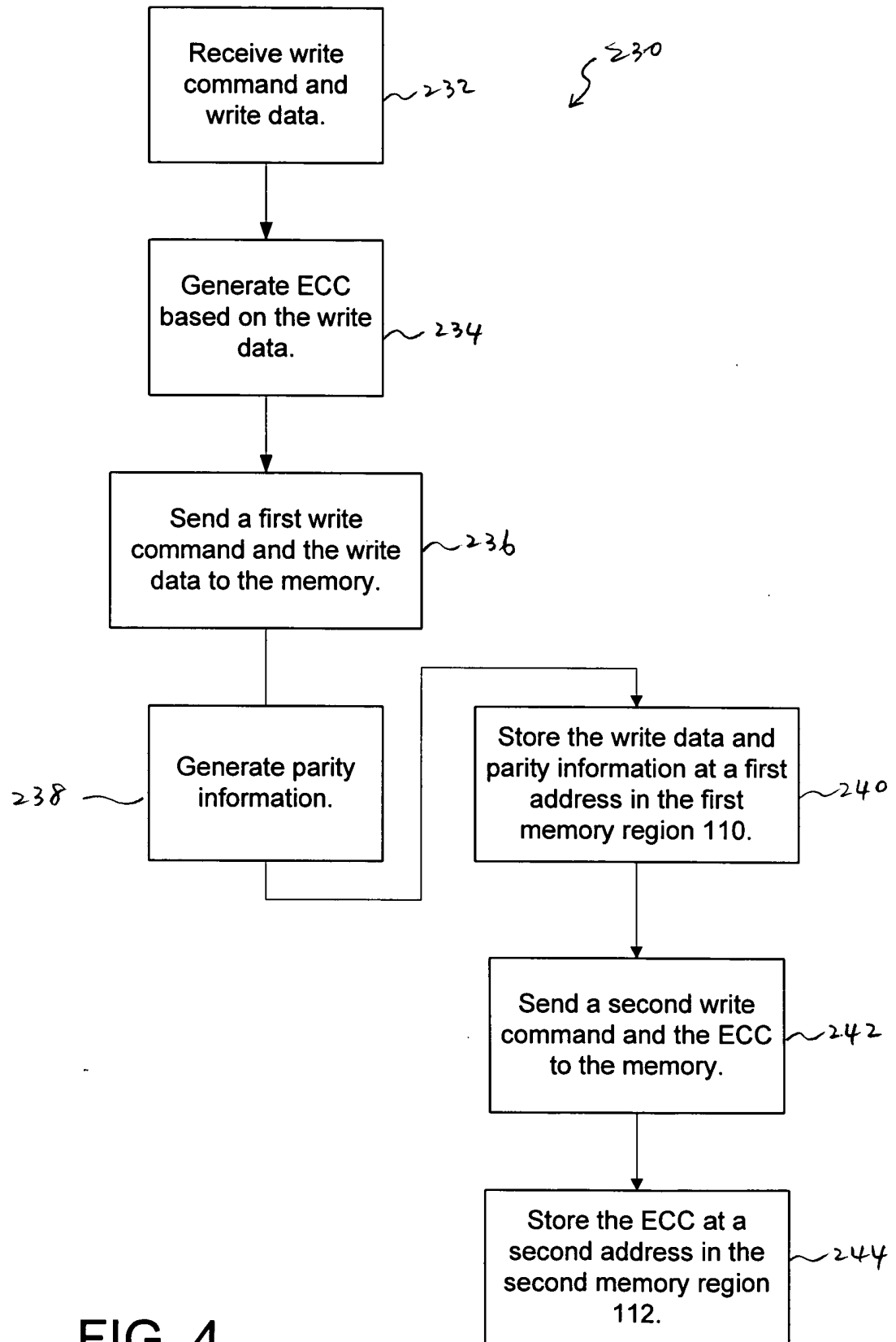


FIG. 4

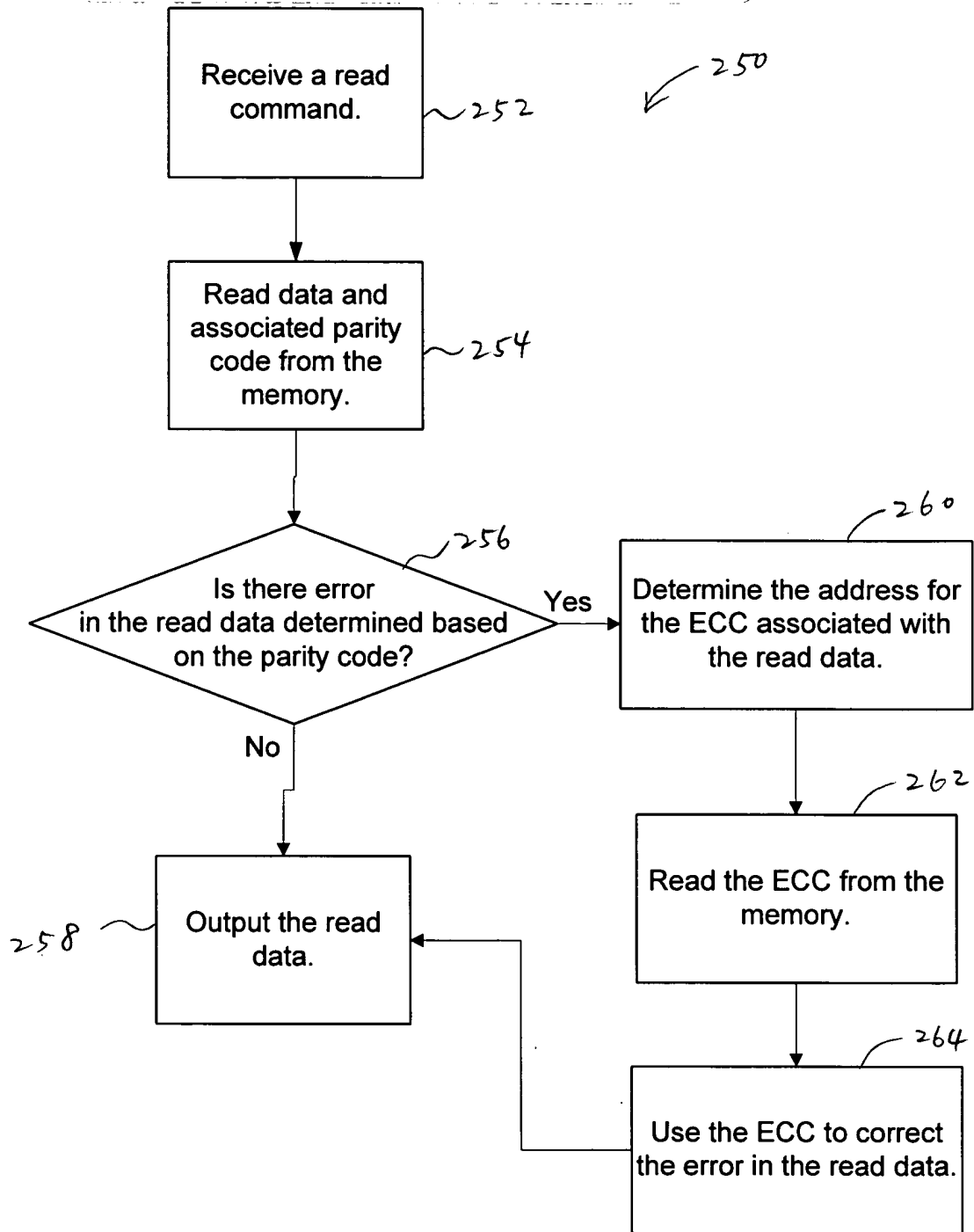


FIG. 5

220  
✓

QDR MEMORY

32bit Data (A0) + 4bit Parity				~ 0x0001
32bit Data(A1) + 4bit Parity				~ 0x0002
32bit Data(A2) + 4bit Parity				~ 0x0003
32bit Data(A3) + 4bit Parity				~ 0x0004
ECC A3	ECC A2	ECC A1	ECC A0	~ 0x0005
32bit Data(B0)+4bit Parity				~ 0x0006
32bit Data(B1) + 4bit Parity				~ 0x0007
32bit Data(B2)+4 bit Parity				~ 0x0008
32bit Data(B3)+4 bit Parity				~ 0x0009
ECC B3	ECC B2	ECC B1	ECC B0	~ 0x000A

Fig. 6

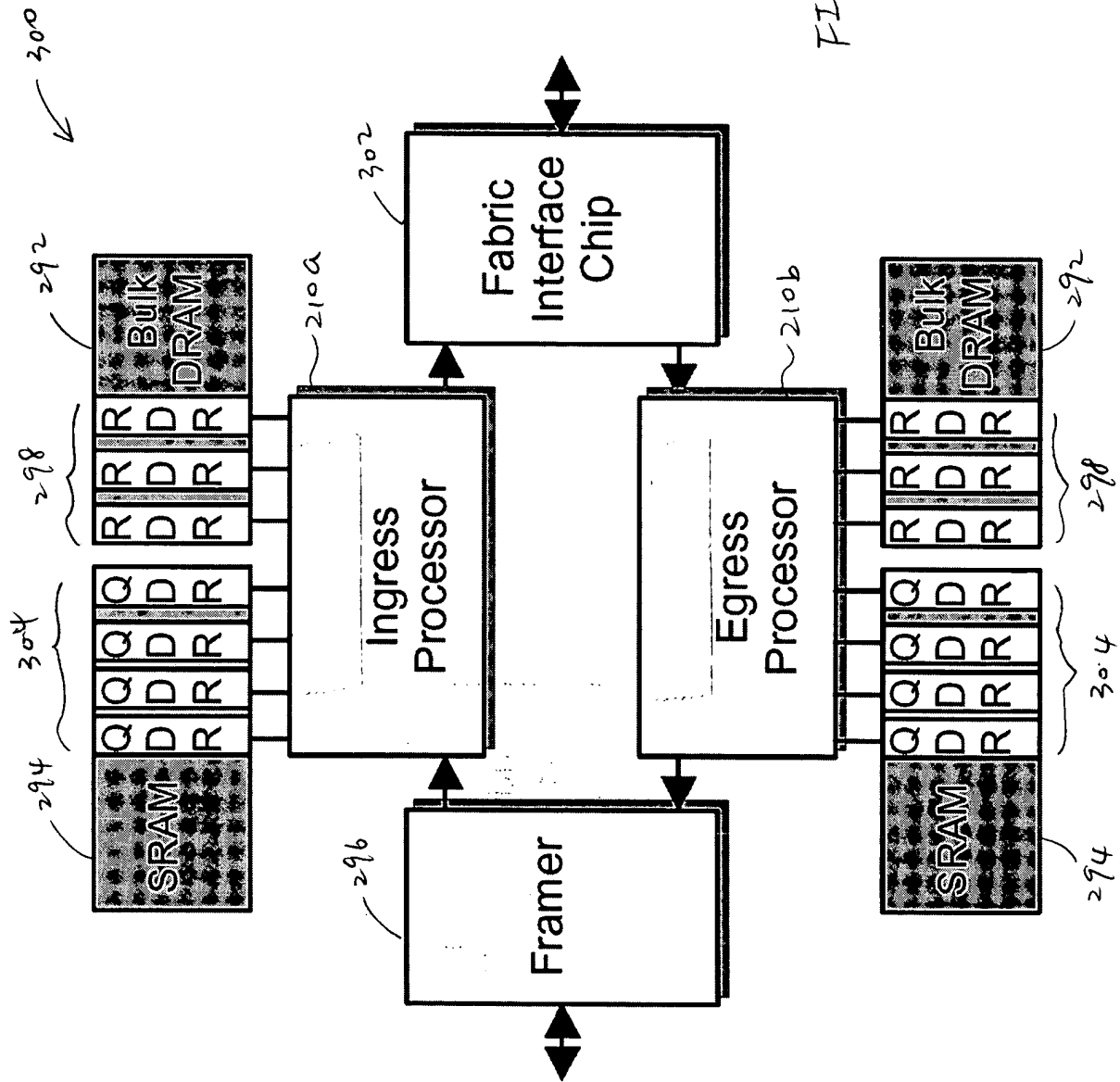


FIG. 7